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Substitute for form 1449/PTO  <b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b>  <i>(Use as many sheets as necessary)</i>				<b>Complete if Known</b>	
				Application Number	09/821,182
				Filing Date	March 29, 2001
				First Named Inventor	Daniel R. Shepard
				Art Unit	2824
				Examiner Name	A. Q. Tran
Sheet	1	of	8	Attorney Docket Number	NUP-001RE

U.S. PATENT DOCUMENTS					
Examiner Initials*	Cite No. <sup>1</sup>	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number-Kind Code <sup>2</sup> (if known)			
	A1*	US-2,709,042	05-24-1955		
	A2*	US-3,091,754	05-28-1963		
	A3*	US-3,245,051	04-05-1966	J. Robb	
	A4*	US-3,373,406	03-01-1968	Cannon et al.	
	A5*	US-3,626,389	12-07-1971	Waaben	
	A6*	US-3,721,964	03-20-1973	Barrett et al.	
	A7*	US-3,838,405	09-24-1974	Arnett et al.	
	A8*	US-3,942,071	03-02-1976	Krebs et al.	
	A9*	US-4,010,453	03-01-1977	Lewis	
	A10*	US-4,090,190	05-16-1978	Rostkovsky et al.	
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	A12*	US-4,308,595	12-29-1981	Houghton	
	A13*	US-4,312,046	01-19-1982	Taylor	
	A14*	US-4,322,822	03-30-1982	McPherson	
	A15*	US-4,342,102	07-27-1982	Puar	
	A16*	US-4,385,368	05-24-1983	Principi et al.	
	A17*	US-4,394,752	07-19-1983	Boudon et al.	
	A18*	US-4,404,480	09-13-1983	Ransom et al.	
	A19*	US-4,442,507	04-10-1984	Roesner	

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	B1	DE-10111454-A1	09-26-2002	Infineon Technologies Ag		√

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	A20*	US-4,479,200	10-23-1984	Sato et al.	
	A21*	US-4,525,921	07-02-1985	Carson et al.	
	A22*	US-4,534,008	08-06-1985	Fuchs et al.	
	A23*	US-4,554,640	11-19-1985	Wong et al.	
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	A25*	US-4,646,128	02-24-1987	Carson et al.	
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	A33*	US-4,845,679	07-04-1989	Vu	
	A34*	US-4,884,238	11-28-1989	Lee et al.	
	A35*	US-5,051,865	09-24-1991	Kato et al.	
	A36*	US-5,163,328	11-17-1992	Holland et al.	
	A37*	US-5,203,731	04-20-1993	Zimmerman	
	A38*	US-5,296,716	03-22-1994	Ovshinsky et al.	

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	A40*	US-5,397,957	03-14-1995	Zimmerman	
	A41*	US-5,432,729	07-11-1995	Carson et al.	
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	A58*	US-6,185,122	02-06-2001	Johnson et al.	
	A59*	US-6,198,682	03-06-2001	Proebsting	
	A60*	US-6,236,587	05-22-2001	Gudesen et al.	
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	A68*	US-6,598,164	07-22-2003	Shepard	
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	A73*	US-7,054,219	05-30-2006	Petti et al.	
	A74*	US-7,088,613	08-08-2006	Lue et al.	
	A75*	US-7,149,934-A1	12-12-2006	Shepard	
	A76*	US-7,183,206-A1	02-27-2007	Shepard	

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	A79*	US-7,376,008	05-20-2008	Shepard	
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	A82*	US-7,471,547	12-30-2008	Schloesser	
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	C2	Crawford et al., "An Improved Tunnel Diode Memory System," IBM Journal (July 1963), pp. 199-206.	
	C3	De Graaf et al., "A Novel High-Density Low-Cost Diode Programmable Read only Memory," IEEE IEDM, session 7.6.1 (1996), pp. 189-192.	
	C4	Hodges et al., "Analysis and Design of Digital Integrated Circuits," McGraw-Hill Book Company, pp. 27-32. )	
	C5	Hurst, "A Survey of Published Information on Universal Logic Arrays," Microelectronics and Reliability, vol. 16 (1977), pp. 663-674.	
	C6	International Search Report and Written Opinion in International Patent Application No. PCT/US2007/007792, mailed October 30, 2007.	
	C7	International Search Report and Written Opinion in International Patent Application No. PCT/US07/011020, mailed July 18, 2008 (16 pages).	
	C8	International Search Report and Written Opinion in International Patent Application No. PCT/US2008/075986, mailed January 22, 2009 (13 pages).	
	C9	International Search Report and Written Opinion in International Patent Application No. PCT/US2008/082503, mailed February 25, 2009 (13 pages).	
	C10	McConnell et al., "Diode-Transistor Logic," Digital Electronics, Chapter 5, West Virginia Univ. (2002), pp. 1-14.	

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	C12	Partial International Search Report in International Patent Application No. PCT/US07/011020, mailed April 14, 2008, 6 pages.	
	C13	"Summary Page: National Radio Institute 832," Old-Computers.com, ,http://old-computers.com/museum/computer.asp?c=884&st=1., (April 4, 2007) 2 pages.	
	C14	Wang et al., "High-Current-Density Thin-Film Silicone Diodes Grown at Low Temperature," Applied Phys. Lett., vol. 85(11), pp. 2122-2124 (Sept. 13, 2004).	
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